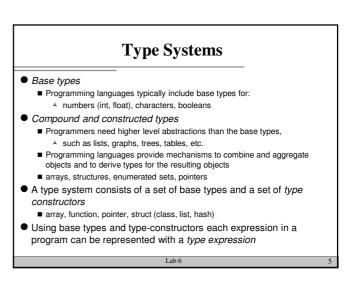
(01JEUHT) Formal Languages and Compilers

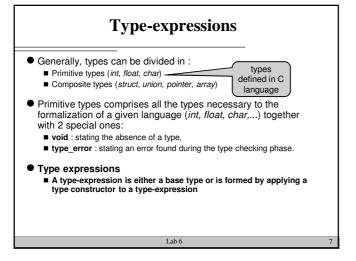
Laboratory N 6

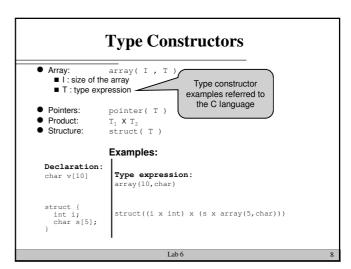
Stefano Scanzio
mail: stefano.scanzio@polito.it

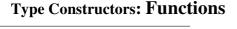
# Type checking Type expressions Symbol tables Implementation of a type-checker

# Type Checking Type Checking is the process used for the verification of types constraints: □ Can be performed at compilation time (static check) or at execution time (dynamic check) □ Dynamic types appear more often in interpreted languages, whereas compiled languages favor static types □ Static checking is one of the main semantic tasks performed by a compiler □ Example of static check: int a; float b; a = 2.5; /\* Correct in c and c++, not correct in Java \*/ b = 1.5; /\* Correct in c and c++, not correct in Java (b=1.5f;)





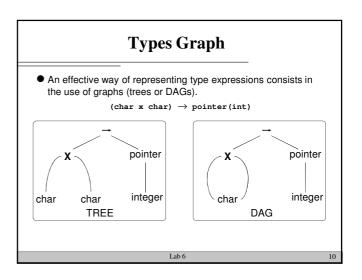


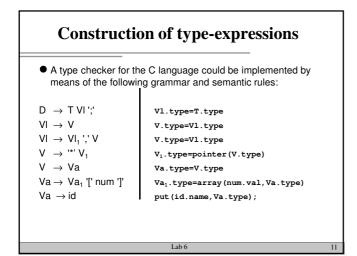


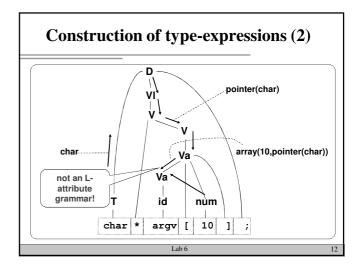
- A function maps an element of its own domain to an element in its own range.
- Functions:  $\mathbf{T}_1 \rightarrow \mathbf{T}_2$
- T₁: domain type
- T<sub>2</sub>: range type
- The function int\* f(char a, char b) is represented using the following type expression:

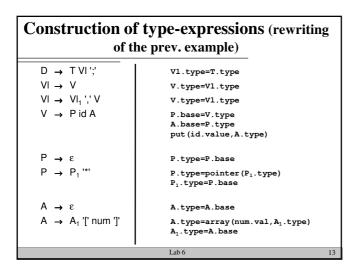
(char x char)  $\rightarrow$  pointer(int)

Lab 6







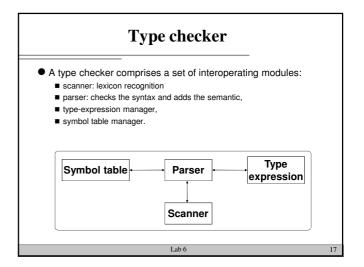


## Types names • In many languages it is possible to assign explicit names to types. Example: typedef cell\* link; type link = ^cell; link p; var p : link; cell\* q; var q : ^cell; Do variables p and q belongs to the same type? Answer depends on the approach used for checking it. ■ Structural equivalence. ■ Names equivalence • In C structural equivalence is used while other languages (e.g., pascal) use names equivalence.

# **Structural Equivalence**

- Two expression are equals if:
  - They belong to the same primitive type
  - They are based on the application of the same types constructors to equivalent types.
- Using a tree based representation for type expression it is possible (and convenient) to use a recursive visit algorithm in order to verify the equivalence.

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# Symbol table

- Symbol tables associates values to names in order to make accessible the semantic information related to an identifier outside of the context where it has been declared.
- Information related to each name are used in order to verify the semantic correctness of identifiers usage within a program.

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# **Designing a Symbol Table**

- A symbol table can be implemented using different data structures:
  - Unordered Lists
  - Ordered Lists
  - Binary Tree
  - Hash Table
  - BTree ...
- This choice is based on the number of symbols to store, on the required performances and on the complexity of the code to be produced.

Lab 6

# Symbol table: HashMap

import java.util.HashMap;

// Initializing the table

HashMap<String, String> symTable = new HashMap<String, String> ();

// Inserting entries: int a; float b;

symTable.put("a", "int"); symTable.put("b", "float");

// Get the value related to key "a"

String tipo = (String) symTable.get("a");

System.out.println(tipo);

// Deleting entry symTable.remove("a");

// Deleting all entries

symTable.clear();

ab 6

# Type expression

- Type expressions (naturally represented by means of trees of types) can be transformed into a different internal representation (i.e., a Class).
- The management of type expressions requires
  - The definition of the data structure associated to each graph node
  - The definition of the data structure associated to a structure associated to the structure associated as the structure associated as the structure as the
- Nodes should be capable of representing the different type constructor and the base types as well.
- Primitives are required in order to hide the internal representation of nodes thus allowing the user to produce the easiest code possible.

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# Implementing type expressions ■ Each node of a types graph comprises: ■ a tag, representing the type of node; ■ A set of different fields depending on the type of data to be stored public class te\_node { public int tag; // BASE, ARRAY, POINTER, ... public int size; // Number of elements in array public int code; // Base type: INT, CHAR, FLOAT, ... // Only for structs public String name;// Struct name // Left and right children private te\_node left, right; }

```
Implementing type expressions

● The module charged to manage Type Expressions should offer the following primitives:

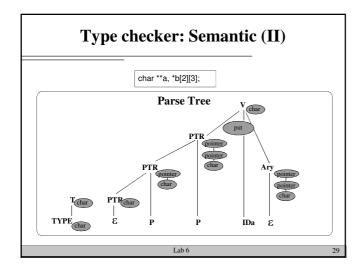
public class te_node {
    public int tag;
    ...
    public static te_node te_make_base(int code);
    public static te_node te_make_pointer(te_node base);

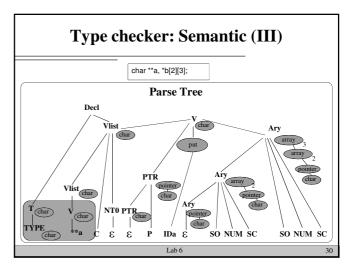
public static te_node te_make_array(int size, te_node base);

// Only for structs and functions
    public static te_node te_make_product(te_node 1, te_node r);
    public static te_node te_make_product(te_node str, te_node flds);
    public static to_node te_make_fwdstruct(String name);
    public static te_node te_make_struct(te_node flds, String n);
    public static te_node te_make_struct(te_node d, te_node r);
}

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```

### Type checker: complete grammar ::= /\* empty \*/ Field ::= T Vlist | S Decl Vlist ::= V | Vlist ',' V Decl ::= T Vlist | TYPEDEF T ID ; V ::= Ptr TD Array ::= TYPE | STRUCT ID '{' SFL '}' | STRUCT '{' SFL '}' ::= /\* empty \*/ Ptr | STRUCT ID Array ::= /\* empty \*/ SFL ::= Field | Array SO NUM SC | SFL Field





# Exam 1

( taste: 10, transparency:2 ) -> honey
.
// Description of the products
wine: \* taste, + perfume = barbera DOC;
wine: \* taste, \* perfume = barolo di annata;
wine: - taste, / perfume = a stinky wine;

honey: \* taste, \* transparency = acacia honey;

// Definition of the product type: ( taste: 12 , perfume: 8 ) -> wine

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### **Thesis**

List: <a href="https://www.skenz.it/ss/theses">https://www.skenz.it/ss/theses</a>
About myself: <a href="https://www.skenz.it/ss">https://www.skenz.it/ss</a>

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# **About myself**

- 2004 Finished my studies at Politecnico di Torino in Computer Science at DAUIN
- 2008 Ph.D. at DAUIN in Automatic Speech Recognition
  - In collaboration with Loquendo (now Nuance)
  - Specifically on Artificial Neural Networks and classification algorithms
- 2009 Research Fellow at IEIIT (institute of the CNR)
  - CNR is the biggest Italian research organization
  - IEIIT institute is in Politecnico di Torino (near room 12, 4° floor)
- 2012 Won a permanent position at CNR as a Researcher

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# Current research activities Industrial Automation

- Communication protocols
  - Industrial networks require a high degree of determinism
  - Easy to obtain in wired networks, but in wireless ones ???
- Real-time operating system (Sometimes most of the indeterminism is inside the PC)
  - Use of real-time extensions of Linux kernel
  - Properly optimize the code (threads, kernel modules, communication between kernel and user spaces)
- Industrial Internet of Things (IIoT)

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# Current research activities Industrial Automation

- Synchronization protocols
  - Nodes must have the same notion of time (μs precision or less)
  - It is a very complex argument that involves the network, operating system, hardware, control algorithms for clock correction, ...
- Machine learning applied to industry
- Complete list of research activities:
  - https://www.skenz.it/ss/research
- Collaborations with: Comau and Ferrero

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# **Programming languages**

- C/C++ for the fastest parts of the code (i.e., applications with real-time requirements)
- Python for post analysis of experimental data or to coordinate experiments
- Linux operating system, and in particular:
  - Linux bash shell
  - Threads
  - Processes

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# For more details...

Read my papers...

https://www.skenz.it/ss/publications

- Click on the paper
- You are redirected to the relevant web page for download
- REMEMBER: a PC inside the network of the "Politecnico di Torino" has to be used (otherwise you cannot access the paper)
- (Citations: <a href="https://scholar.google.it/citations?user=yqyzGToAAAAJ&hl=en">https://scholar.google.it/citations?user=yqyzGToAAAAJ&hl=en</a> )
- Or better call myself (011 090 5438) or write an email
- Or even better...pass into my office
- More details regarding thesis: <a href="https://www.skenz.it/ss/theses">https://www.skenz.it/ss/theses</a>

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